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ADC1443D200W1 ADC1443D200W1 Demo board With ARRIAII GX FPGA

IDT'S ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration. The FPGA eases the evaluation and analysis of the ADC dynamic and enables use of the full JESD204B feature set.



EVALUATION BOARD
(NO IMAGE AVAILABLE)

Status: **Active**

Product Quick Links

- Product Options
- Documentation & Tools
- Related Products
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Features

- SMA connector for clock and analog input signals
- On board Data clock generator for ADC and FPGA
- On board ARRIAI GX FPGA for JESD204B data acquisition
- 2 dual ADC on same PCB to easily evaluate the Multi ADC synchronization
- Single power supply (on-board regulators)

Product Options

Orderable Part ID	Part Status	Pkg. Code	Pkg. Type	Lead Count (#)	Temp. Grade	Pb (Lead) Free	Sample & Buy
ADC1443D200W1-DB	Active	BD0	BOARD	0	C	No	Check Availability

Documents

	Title	Type ▲	Format	File Size	Date
	Demo Board Software ADC4	Manual - Software	ZIP	4.12 MB	07/18/2012

Related Products

Product ID	Title
ADC1443D200HD	Dual channel 14-bit ADC; 200 Msps; JESD204B serial outputs